



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Martin Blumenfeld et al. Art Unit : 1655
Serial No. : 09/853,806 Examiner : F. Lu
Filed : May 11, 2001
Title : APPARATUS FOR GENERATING A TEMPERATURE GRADIENT AND
METHODS FOR USING THE GRADIENT TO CHARACTERIZE
MOLECULAR INTERACTIONS

Commissioner for Patents
Washington, DC 20231

TRANSMITTAL LETTER

The following correspondence relating to this application is enclosed for filing:

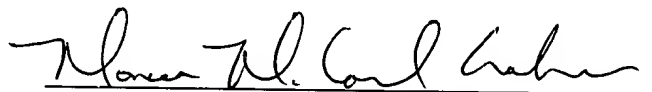
1. Amendment and Response;
2. Form 1449 that accompanied the Information Disclosure Statement of August 13, 2002;
3. Petition for Three-Month Extension of Time;
4. Check in the amount of \$465.00; and
5. Return Postcard.

Please date stamp and return the enclosed postcard. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

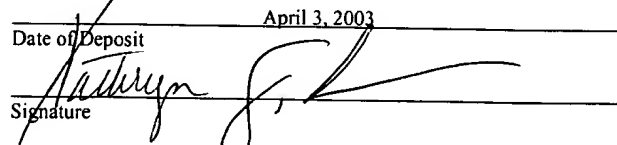
Date: 4/3/03

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit April 3, 2003
Signature 
Kathryn J. Sanderson
Typed or Printed Name of Person Signing Certificate



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Martin Blumenfeld et al. Art Unit : 1634
Serial No. : 09/853,806 Examiner : F. Lu
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METHODS FOR USING THE GRADIENT TO CHARACTERIZE
MOLECULAR INTERACTIONS

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AMENDMENT AND RESPONSE

In response to the action mailed October 3, 2002, please amend the application as follows:

In the claims:

Please amend claims 23, 31, and 73 as follows:

-- 23. (Amended) A method of generating a temperature gradient comprising:
attaching two electrical connectors to a semiconducting wafer, wherein each of the
connectors are adjacent to each other and attached to the wafer at an attachment site with a gap
disposed between the attachment sites; and

connecting a power source to the wafer through the two electric connectors to cause
current flow through the wafer and a continuous temperature gradient to be formed on a surface
of the wafer.

31. (Amended) The method of claim 30 wherein the stratum comprises a material
having a thermal conductivity that is significantly lower than that of the surface of the wafer.

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